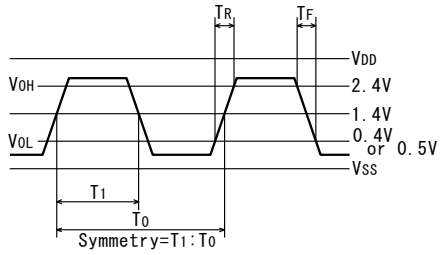


# Crystal Oscillators

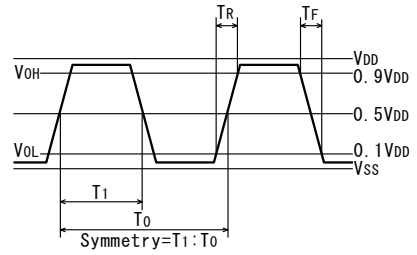
水晶発振器

## Crystal Clock Oscillators クロックオシレータ

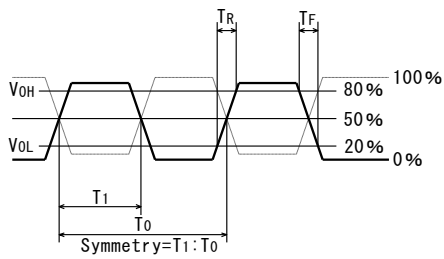
### Output Wave Forms 出力波形



TTL



CMOS



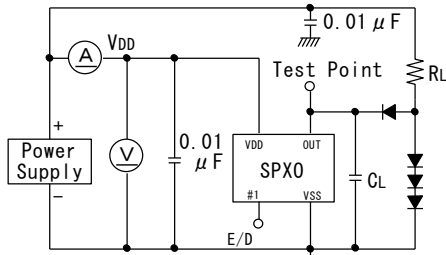
PECL/LVDS/HCSL

# Crystal Oscillators

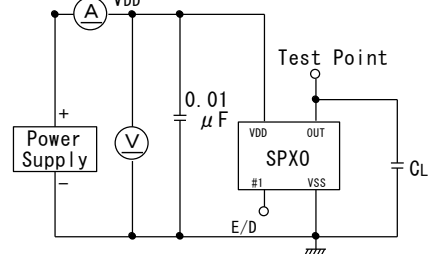
水晶発振器

## Crystal Clock Oscillators クロックオシレータ

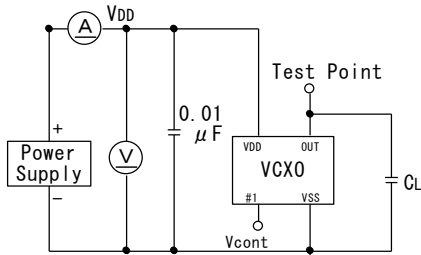
### Test Circuit Examples テスト回路例



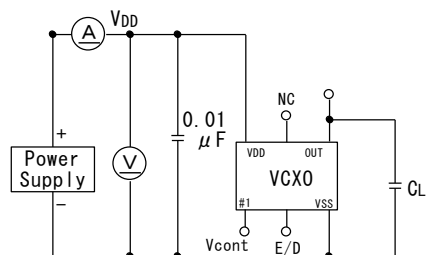
TTL



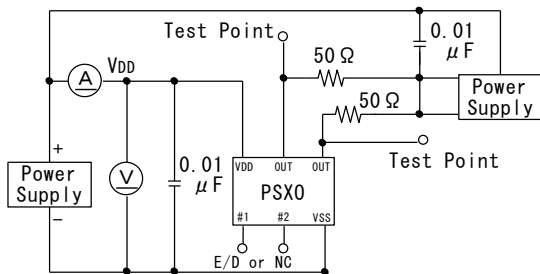
CMOS



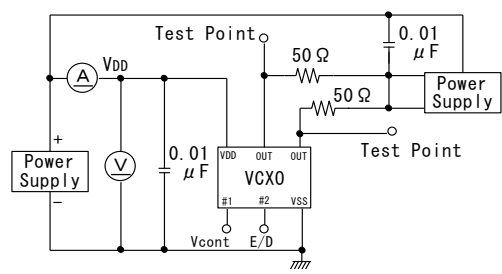
CMOS(VCXO)4pads



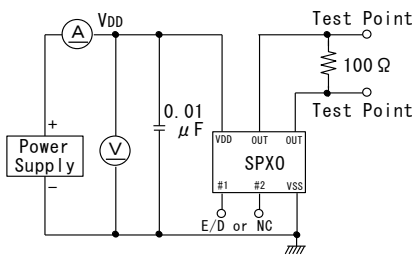
CMOS(VCXO)6pads



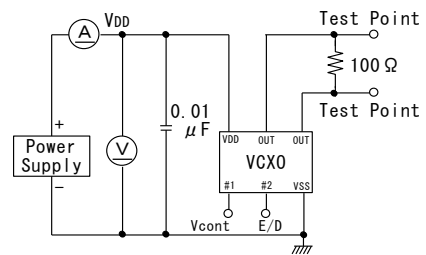
PECL(XO)



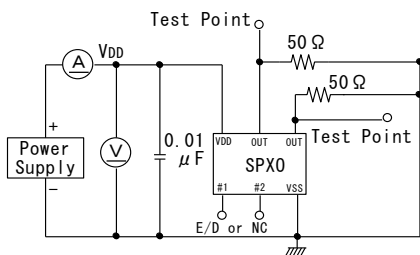
PECL(VCXO)



LVDS(XO)



LVDS(VCXO)



HCSL

- \* When using clock oscillators, insert a pass capacitor of about 0.01  $\mu$ F or 0.1  $\mu$ F between(Vdd) and ground(Vss).
- \* ご使用の際は電源(Vdd)、グラウンド(Vss)間に0.01  $\mu$ Fないし0.1  $\mu$ F程度のバイパスコンデンサを入れてください。